

Electronic Acknowledgement Receipt

EFS ID:	3068072
Application Number:	10748906
International Application Number:	
Confirmation Number:	7419
Title of Invention:	Semiconductor memory device testable with a single data rate and/or dual data rate pattern in a merged data input/output pin test mode
First Named Inventor/Applicant Name:	Jong-Cheol Lee
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Filer:	Frank Chau/MARG dougan
Filer Authorized By:	Frank Chau
Attorney Docket Number:	8836-223JHM/ID12244US
Receipt Date:	28-MAR-2008
Filing Date:	30-DEC-2003
Time Stamp:	12:16:24
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	122807-OA-AMENDMENT-8 836-223.pdf	268727 <small>168210a1fba47ab6c5007b1846060d 608427e1</small>	no	8

Warnings:

Information:

2	Examination support document	122807-OA-AMENDMENT-E XHIBIT-A-8836-223.pdf	889408 <small>5d95524aa64eeda7603e26df480ee8ab 1e9c3545</small>	no	4
Warnings:					
Information:					
3	Drawings-only black and white line drawings	FIG-2-REPLACE-SHEET-88 36-223.pdf	14578 <small>a276a419baeed4e7ea3ca331e5dc58ad 8d3933d</small>	no	1
Warnings:					
Information:					
Total Files Size (in bytes):			1172713		
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